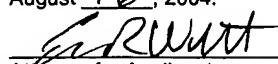




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CERTIFICATE OF MAILING

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E. R. Wutt
Attorney for Applicants

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/849,347
Applicant : Robert H. Burgener, II et al.
Title : FABRICATION OF P-TYPE GROUP II-VI SEMICONDUCTORS
Filed : May 19, 2004
TC/A.U. : (not yet assigned)
Examiner : (not yet assigned)
Docket No. : 3398.2.8
Customer No. : 21552

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Transmitted herewith is an Information Disclosure Statement disclosing information which has come to the attention of applicants and/or their attorneys and is being submitted so as to comply with the duty of disclosure set forth in 37 C.F.R. § 1.56. In accordance with 37 C.F.R. § 1.97(b), the enclosed Statement is being filed within three (3) months of the filing date of the above-identified application or before the mailing date of a first Action on the merits.

Neither applicants nor their attorneys make any representation that any information disclosed herein may be "prior art" within the meaning of that term under 35 U.S.C. § 102 or § 103. Moreover, pursuant to 37 C.F.R. § 1.97, the filing of this Information Disclosure Statement

shall not be construed as a representation that a search has been made or as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R. § 1.98, transmitted herewith are:

1. A completed copy of Forms PTO/SB/08a and/or PTO/SB08b "Information Disclosure Statement by Applicant" listing the patents, publications and other information being submitted for consideration; and
2. A legible copy of each patent, publication and other item of information in written form listed on the enclosed Forms PTO/SB/08a and PTO/SB/08b, except for copies of U.S. patents and published U.S. patent applications which are not required for applications filed after June 30, 2003.

As this application was filed after June 30, 2003, copies of the U.S. patents and published U.S. patent applications listed on the enclosed Form PTO/SB/08a are not required and, therefore, not included herewith.

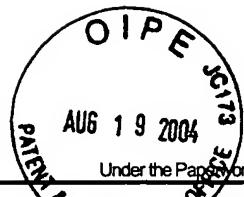
Respectfully submitted,



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Attorney for Applicants

Date: August 16, 2004

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PTO/SB/08a (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 Of 1

Complete if Known

Application Number	10/849,347
Filing Date	May 19, 2004
First Named Inventor	Robert H. Burgener, II
Group Art Unit	
Examiner Name	
Attorney Docket Number	3398.2.8

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
U1	US-2003/0226499		12/11/2003	Teherani	
U2	US-6,589,362-B2		07/08/2003	Haga	
U3	US-6,610,141-B2		08/26/2003	White et al.	
U4	US-6,617,183-B2		09/09/2003	Kadota et al.	
U5	US-6,624,441-B2		09/23/2003	Cantwell et al.	
U6	US-6,664,565-B1		12/16/2003	Sano et al.	
U7	US-6,673,478-B2		01/06/2004	Kato et al.	

Examiner Signature	Date Considered
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Substitute for form 1449B/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/849,347
(use as many sheets as necessary)		Filing Date	May 19, 2004
Sheet 1 Of 1		First Named Inventor	Robert H. Burgener, II
		Group Art Unit	
		Examiner Name	
		Attorney Docket Number	3398.2.8

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
	O1	Itani, Kenya, et al., "Low-Dislocation-Density GaAs Wafers Grown by Vertical Gradient Freeze Process, Suitable for Mass Production of Semiconductor Lasers," Hitachi Cable Review No. 20, August 2001, pp. 35-38		
	O2	Look, D.C., et al. "The Future of ZnO Light Emitters," Phys. Stat. Sol. (2004), pp. 1-10		
	O3	Pearson, S.J., et al. "Wide Band Gap Ferromagnetic Semiconductors and Oxides," Journal of Applied Physics Vol. 93, No. 1, January 1, 2003, pp. 1-13		
	O4	Web Page '99 SBIR Phase I... Topic 14 – Electronic Materials, http://www-ee.eng.buffalo.edu/faculty/paololin/492/semicon.pdf , "Low-Temperature Hydrothermal Growth of ZnO for Semiconductor Substrates," 2/26/04, 2 pages		
	O5	Web Page "Semiconductor Photonics", 3 pages		

Examiner Signature	Date Considered
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